

Institution	EAST POINT COLLEGE OF ENGINEERING AND TECHNOLOGY				
Department	ELECTRONICS AND COMMUNICATION ENGINEERING				
Event Type / Theme Please Tick (v) appropriate	ClubActivities/IndustrialVisit/Guest Lecture/InvitedTalk/Workshop/FDP/Seminar / Webinar / Conference /Sports Event /NSS Activity / Cultural EventOthers(PleaseMention): Workshop				
Event Title	"Cadence EDA Tools"				
Date	27/042025	28/042025	Time	08:45 AM	
Venue	Online				
Duration	7hr	From:	08:45 AM	To:	4:00 PM
Program Objective	1. To provide participants with a fundamental understanding of Electronic Design Automation (EDA) tools used in VLSI design. 2. To familiarize participants with the Cadence design environment and its applications in the semiconductor industry. 3. To impart practical knowledge of schematic design, circuit simulation, and analysis using Cadence tools. 4. To introduce participants to the complete IC design flow, including design entry, verification, layout, and validation. 5. To develop hands-on skills in analog and digital circuit design using industry-standard EDA software.				
1. Gained an understanding of the fundamentals of Electronic Design Automation (EDA) and its importance in VLSI design. 2. Acquired knowledge of the Cadence design environment and its industry applications. 3. Learned the complete integrated circuit (IC) design flow from schematic design to verification. 4. Developed hands-on skills in schematic capture and circuit simulation using Cadence tools. 5. Understood layout design techniques and physical verification processes. 6. Gained practical experience in performing Design Rule Check (DRC) and Layout Versus Schematic (LVS) verification.					
1.Namesof the Resource Persons/Chief Guests /Guests of Honors/Other's Mention			Mr. Sanjay G S		
2.Designation/s			Field Application Engineer		
3.Company'sName/Associations			Entuple Technologies Pvt. Ltd		
Total Number of Participants			116		
Total number of Participants in Google Meet			116		

Total number of feedbacks received through Google forms from both Google meet and YouTube	-
Total Number of Views(FirstDay)in You Tube	NA
Photos	 EAST POINT COLLEGE OF ENGINEERING & TECHNOLOGY AN AUTONOMOUS INSTITUTION, AFFILIATED TO VTU, APPROVED BY AICTE ACCREDITED BY NATIONAL BOARD OF ACCREDITATION (NBA) - CSE, ECE & ISE DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING Conducting 2 days workshop on CADENCE EDA TOOLS Resource Person Mr. Sanjay G S Field Application Engineer, Entuple Technologies pvt. Ltd., Bengaluru. Mrs. Parvati patil Mrs. Sandhya N Faculty Co-ordinators Dr. Yogesh G S Vice-Principal & HOD ECE, EPCET Dr. Pradipkumar Dixit Principal, EPCET VENUE DATE TIME VLSI Lab 27th & 28th April 2026 10:00 AM to 04:00 PM  Bengaluru, Karnataka, India Jnanaprabha, East Point Campus, Post, Virgo Nagar, Avanehalli, Bengaluru, Karnataka 560049, India Lat 13.050352° Long 77.714763° Tuesday, 27/04/2026 03:11 PM GMT +05:30  Bengaluru, Karnataka, India Jnanaprabha, East Point Campus, Post, Virgo Nagar, Avanehalli, Bengaluru, Karnataka 560049, India Lat 13.050352° Long 77.714763° Tuesday, 28/04/2026 03:59 PM GMT +05:30
Video Recording Link/You Tube Link	NA