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- Dr. Prahlada Rao did his Ph.D (1995) in Computer Science & Automation from Indian Institute of Science, Bangalore, M.Tech (1981) in Control Systems Engineering from Indian Institute of Technology, Kharagpur and B.Tech (1979) in Electrical & Electronics Engineering from Jawaharlal Nehru Tech. University-Kakinada.
- **He Served** in Industry IT R&D for 28+ years, and Academics in Computer Science (8+ Years). Served as Dean, Computer Science at RGMCET-Nandyala Andhra Pradesh, Adjunct Professor at IIITDM-Kurnool. Dean, Computer Science at MITS-Madanapalle and Distinguished Professor at GITAM-Bangalore, and Professor IIIT Bangalore and on deputation served at MIIT-Myanmar, Mandalay
- **Worked in Industry** IT R&D, with significant contributions to IT, Computer Science: EDA Algorithms and Avionics at MNC's: IBM Global Services Ltd., STmicoelectronics, Mentor Graphics India Pvt. Ltd., and Government of India's Research Institutes: C-DAC Bangalore, HAL Hyderabad, ADA Bangalore.
- **His Areas of Specialization:** Artificial Intelligence, ML, Scientific Cloud Computing, High Performance Computing, Hybrid Computing, Computer Architectures. Grid Computing. e-Infrastructures for Scientific Computing, EDA Tools Development, EDA Algorithms and Avionics. Evolutionary Computing, Genetic Algorithms and Simulated Annealing, EDA Algorithms. Optimization,
- He Published 5 Patents and received 1-US Patent Granted and 3 Granted IPO Patents. He Authored more than 70+ Papers that are published in reputed journals in Elsevier, Springer, IEEE, WOS & Scopus.
- He has been awarded a fund of Rs. 800L for Cloud Computing Research & Development Projects from MEITY, Govt of India and 75 Crores Grid Computing R&D Projects from MEITY, Govt of India and DST, Govt of India, Rs. 10L to Organize an International Workshop on Cloud Computing in 2011-12. Rs. 4 Laks FP7-Project-287820 EU-INCOOP Project Fostering Co-

	Operation in: EU-India Computing Systems FORTH-Greece, KYOS- Switzerland, Open Group-UK, IISc, C-DAC, ITSMA-Bangalore, (1st Oct 2011 to 30 Nov 2013) • **He was involved as Member-Board of Governers and Member-Bboard of Studies** RGMCET Nandyal, 2020-24.*NITEE Meenakshi* (CSA, IT Dept), Bangalore, 2012-14. *Vigyan Engg. College*, Guntur (BOS for Ph.D, M.Tech, B.Tech) 2010-11. *VIT, Vellore* BOS (CSA, IT Department) 2009-10. Served as **Member** of Technical Experts Council IBM Academy of Technology 2003-04. • **Task Force Member:** BigData Analytics, Super Computing, Cloud & Grid Computing, P-CMM at IBM (2003-04), FPGA, and ISO at STMicroelictronics, 1998-2000. • **Served as Grievance Officer** of C-DAC (2014-15) Chairman, Material Management Committee: Procurements & Purchases (2010-15), Chairman, Medical Fascilities Committee: Procurements & Purchases(2014-15) C-DAC Bangalore. • **Served as Extended Faculty** Member**, BITS-Pilani:** for MS-VLSI Program of BITS-Pilani, Bangalore 2002-05. • **Served as Advisory/Progam Committee Member** for International & National Conferences on: Big Data, Cloud Computing, Grid Computing, Evolutionary Computation, Parallel & Distributed Computing and VLSI Design. **Patents Granted:** • B.B.Prahlada Rao and Srinivasa Patil , "Performance Groups-Based Fast Simulated Annealing that improves Speed and Quality of VLSI Placement", US 6725437B1, Granted Patent, Dated: April. 20, 2004. USA. https://patents.google.com/patent/US6725437 • MohanRam N, Prahlada Rao B. B , Mangala N, Sridharan R, Asvija B, Shamjith K.V, "A method and system for Debugging or run time profiling of heterogeneous computation al grids and/or geographically distributed systems", IPO Filed# 02610/CHE/2007, Dated of Filing: 09/11/2007, Granted Patent # 275095, dated: 23/08/2016, IPO • Shamjit KV, Mangala N, Deepika K.V., Prachi Pande, Prahlada Rao B.B., Sarat Chandra Babu N., Method And System For Dynamic Adaptation Of Program Execution On Differenct Target Hardware", Application Nu # 4973/CHE/2014, Dated: 01/10/2014, Granted Patent # 415299, dated: 2021, IPO • Payal Saluja, Bhaskara Prahlada Rao B.,Sarat Chandra Babu "A CLOUD STORAGE SERVICE FOR CLOUD COMPUTING", Indian Paten Application Nu. # 1392/CHE/2013, Filed Dated**:** 28/03/2013, IPO. Published in 2014.
	Book Chapters

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2. Vineeth S Arackal, Arunachalam B, Kalasagar B, Sumit Kumar, Mangala N, Sarat Chandra Babu, Prahlada Rao B.B., Sukeshini, SuMegha Cloud Kit: Create Your Own Private Scientific Cloud, International Symposium on Grids and Clouds 2016 [**ISGC 2016**], Academia Sinica, Taipei, Taiwan, 13-18 March 2016, DOI:10.22323/1.270.0024, Corpus ID: 55863444.
3. Prahlada Rao.. .et all., **A Framework and Roadmap for Cloud Computing Innovation in India**, **White Paper 2.0,** Cloud Computing Innovation Council of India [CCICI], **IEEE Standards Association, IEEE-SA Industry** Connections, vol., no., pp.1-206, 31 Dec. 2014, DOI: **10.1109/IEEESTD.2014.7039186.**
4. Shamjith K. V., Asvija B., Sridharan R., Prahlada Rao BB., Mohanram N., Realizing Inter-operability among Grids: A Case Study with GARUDA Grid and the EGEE Grid, International Symposium on Grid Computing 2008, Taipei, Taiwan, 7-11 April 2008. **Production Grids in Asia, 2010**, pp 175-184, Date: 30 Sep 2009, Production Grids in Asia , DOI: 10.1007/978-1-4419-0046-3_14
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6. B.B. Prahlada Rao, **Evolutionary Approaches to VLSI Channel Routing, Ph.D Dissertation**, CSA Department, Indian Institute of Science, Bangalore, India, Dec.1994, https://iisc.ac.in/doi/abs/10.13140/RG.2.2.23551.00163
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9. Prahlada Rao B. B., L. Patnaik, R. Hansdah, An Extended Evolutionary Programming Algorithm for VLSI Channel Routing, 1995, San Diego, CA, USA, pp. 521-544. 1-3 March 1995, **Book Chapter, MIT Press,** Corpus ID: 6894613, **DOI:** 10.7551/mitpress/2887.003.0047, 1995 URL: http://www.arnetminer.org/viewpub.do?pid=182678

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3. S. Janakiraman, Mohit Ved, Ramesh Naidu Laveti, and B.B. Prahlada Rao, 'Seasonal Forecasting of Indian Summer Monsoon by NCEP GFS atmosphere model' National Conf. on TROPMET-2011, India Meteorological Society [IMS] Hyderabad. Dec14-16, 2011, Special Issue: Grid Infrastructures Supercomputing Conference - 2011
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Achievements / Awards / Recognitions

- SKOCH Smart Governance Award 2015 Order of Merit-SuMegha Scientific Cloud: A Scientific Cloud Stack Product developed during the working at C-DAC, Bangalore, Sept 2015, SKOCH Group, New Delhi
- SKOCH Smart Governance Award 2015 Order of Merit for CloudVault-A Cloud Storage Solution Product developed during the working at C-DAC, Bangalore, Sept 2015, SKOCH Group, New Delhi
- Best Project Award to GARUDA Grid, in International Conference on eScience-2008, Bangalore
- Best Parallel Application Award: 'Parallel Genetic Algorithms Based VLSI Channel Routing', Parallel Comp Research Implemented during the Ph.D Study at I.I.Sc. Bangalore, 1990-95

	• Best Employee of FPGA Team- 2020, STMicroelectronics Noida
	Professional Societies Membership • Life Member- Computing Society of India (CSI), India • Life Member- Advanced Computing and Communication Society (ACCS) India • Member-Technical Experts Council IBM Academy of Technology, 2003-04 • EC Member- Cloud Innovation Council of India, IEEE Standards Association 2013-15