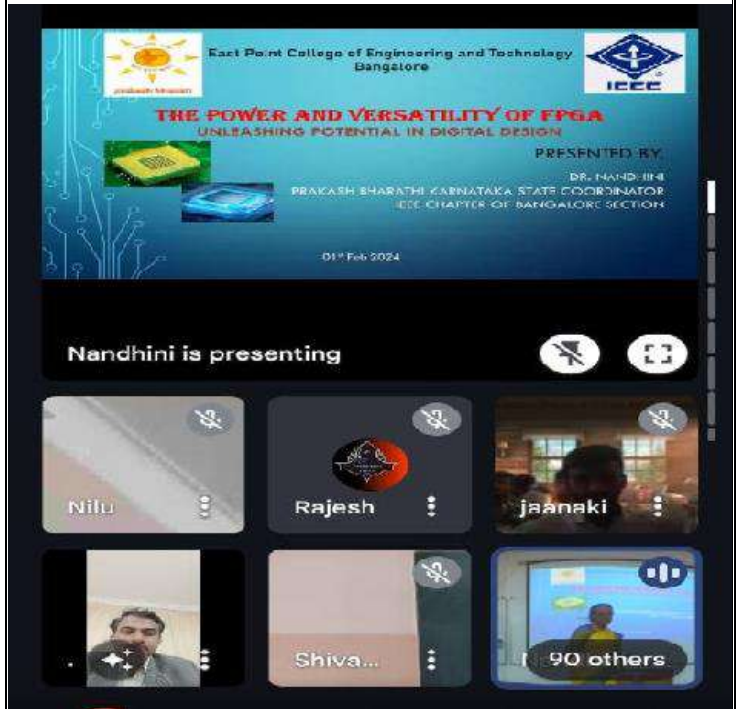


Institution	EAST POINT COLLEGE OF ENGINEERING AND TECHNOLOGY				
Department	ELECTRONICS AND COMMUNICATION ENGINEERING				
Event Type / Theme Please Tick (v) appropriate one	Club Activities/ Industrial Visit/ Guest Lecture/ Invited Talk/ Workshop / FDP /Seminar / Webinar / Conference /Sports Event /NSS Activity / Cultural Event Others (Please Mention): Webinar				
Event Title	The Power and Versatility of FPGA				
Date	1.02.2024		Time	11:00 AM	
Venue	ECE SEMINAR HALL				
Duration	<u>1.5 Hrs</u>	From:	11.00 AM	To:	12.30PM
Program Objective	The objective of a program focused on the power and versatility of Field-Programmable Gate Arrays (FPGAs) is to explore and harness the unique capabilities offered by FPGA technology. FPGAs are programmable integrated circuits that allow users to define and implement digital logic circuits, enabling flexible and customizable hardware solutions.				
All the students were able to understand the FPGA Architecture, Design and Implementation, Applications and Use Cases, Parallel Processing and Acceleration, Communication Protocols and Interfaces, Real-Time Systems and Responsiveness					
1. Names of the Resource Persons / Chief Guests / Guests of Honors / Other's Mention	Dr Nandhini				
2. Designation/s	Karnataka State Coordinator,				
3. Company's Name / Associations	Prakash Bharathi Society				
Total Number of Participants	100				
Total number of Participants in <i>Google</i> Meet	NA				
Total number of feedbacks received through Google forms from both Google meet and YouTube	NA				
Total Number of Views (First Day) in YouTube	NA				

Photos



	 <p>Bengaluru, Karnataka, India Jnana Prabha, East Point Campus, Virgo Nagar Post, Avalahalli, Chennacandra, Bengaluru, Karnataka 560049, India Lat 13.05016° Long 77.714134° 01/02/24 11:23 AM GMT +05:30</p>
Video Recording Link / YouTube Link	: (In case of virtual event)