


Annexure -1

<h2 style="text-align: center;">Faculty</h2>	<p>Shivakumar Badiger Assistant professor East Point College of Engineering & Technology</p>
	<p>Profile*</p> <p>Prof. Shivakumar Badiger holds a Masters in VLSI & Embedded System from VTU Belgaum. Industry Semiconductor, Design FP Methodologies. RTL, CAD, SYNOPSIS, CADENCE.</p>
	<p>Publications</p>
	<p>Books</p>
	<ul style="list-style-type: none"> •
	<p>Journals</p>
	<ul style="list-style-type: none"> •
	<p>Magazines</p>
	<ul style="list-style-type: none"> •
	<p>Conferences</p>
<ul style="list-style-type: none"> • 	
<p>Achievements / Awards / Recognitions</p>	
<ul style="list-style-type: none"> • 	