

VLSI BASED CAREER READINEES PROGRAM

BROCHURE:



The brochure features a light gray background with a decorative border of water droplets. At the top center is the East Point College of Engineering & Technology logo, which consists of two stylized faces in a circle. Below the logo, the text reads "EAST POINT COLLEGE OF ENGINEERING & TECHNOLOGY" and "DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING". The main title "VLSI BASED CAREER READINESS PROGRAM" is written in red, bold, uppercase letters. To the left of the title, the event details are listed: "Date: 05th February 2020", "Venue: M J Seminar Hall", "Resource Person: Mr. Vijendra", and "Corel Technologies". In the center, there is a photograph of a blue printed circuit board (PCB) with various electronic components. At the bottom, the names and titles of the organizers are listed: "Dr Yogesh G S, HOD, Dept. of ECE" on the left and "Dr Prakash S, Principal, EPCET" on the right.

 **EAST POINT** COLLEGE OF ENGINEERING &
TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Presents
SEMINAR ON
VLSI BASED CAREER READINESS PROGRAM

Date: 05th February 2020
Venue: M J Seminar Hall
Resource Person: Mr. Vijendra
Corel Technologies

Dr Yogesh G S
HOD, Dept. of ECE

Dr Prakash S
Principal, EPCET

RESOURCE PERSON:

Mr VIJENDRA

SENIOR TEAM LEAD

COREL TECHNOLOGIES

DATE: 5TH FEBRUARY 2020

VENUE: M J SEMINAR HALL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING HIGHLIGHTS:

This training program is arranged to develop the skills that may be necessary for fresh engineers to pursue careers in high-technology VLSI Engineering domain. Subsequent to the successful completion of this program **placement assistance will be provided** to pursue further their employment prospects in VLSI & Embedded Design companies.

How do you map this with Academic gaps

- The talk was given QSOCS technologies on the current demand on VLSI stream in the industry and benefits of certifying a course on VLSI
- They can learn Xilinx tool and mentor Graphics apart from the syllabus.
- Chip design.
- ZYNQ.
- Prototyping and Implementation Workflow for FPGA/ASIC Design.
- PYNQ Framework and PYNQ-Z2 Development Board

SNAPS OF THE DAY:



**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
CERTIFICATES:**

 **EAST POINT COLLEGE OF ENGINEERING & TECHNOLOGY**

Department of Electronics And Communication Engineering

CERTIFICATE OF PARTICIPATION

This is to certify that
ABHIJITH
Has participated
In
Talk on
VLSI based Career awareness program
Held On February 05th, 2020
At East Point College of Engineering and Technology, Bengaluru, Karnataka,
India


Dr. Yogesh G S
HOD, ECE ,EPCET


Dr. Prakash S
PRINCIPAL
EPCET

 **EAST POINT COLLEGE OF ENGINEERING & TECHNOLOGY**

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