VLSI BASED CAREER READINEES PROGRAM

BROCHURE:

RESOURCE PERSON:

Mr VIJENDRA

SENIOR TEAM LEAD

COREL TECHNOLOGIES

DATE: 5TH FEBRUARY 2020

VENUE: M J SEMINAR HALL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
HIGHLIGHTS:

This training program is arranged to develop the skills that may be necessary for fresh engineers to pursue careers in high-technology VLSI Engineering domain. Subsequent to the successful completion of this program **placement assistance will be provided** to pursue further their employment prospects in VLSI & Embedded Design companies.

How do you map this with Academic gaps

➢ The talk was given QSOCS technologies on the current demand on VLSI stream in the industry and benefits of certifying a course on VLSI

➢ They can learn Xilinx tool and mentor Graphics apart from the syllabus.

➢ Chip design.

➢ ZYNQ.

➢ Prototyping and Implementation Workflow for FPGA/ASIC Design.

➢ PYNQ Framework and PYNQ-Z2 Development Board

SNAPS OF THE DAY:

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CERTIFICATES: